FIG. 1

貴社No. F008765
特所No. EP9003

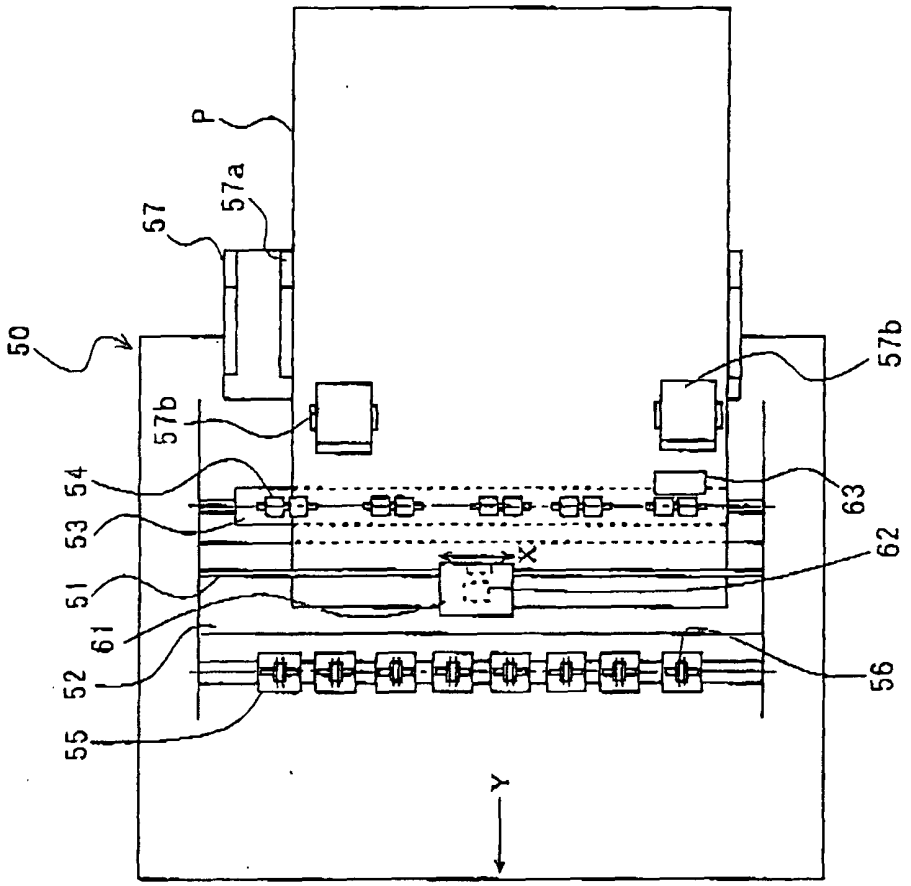


FIG. 2

貴社No. P008765
弊所No. EP9003

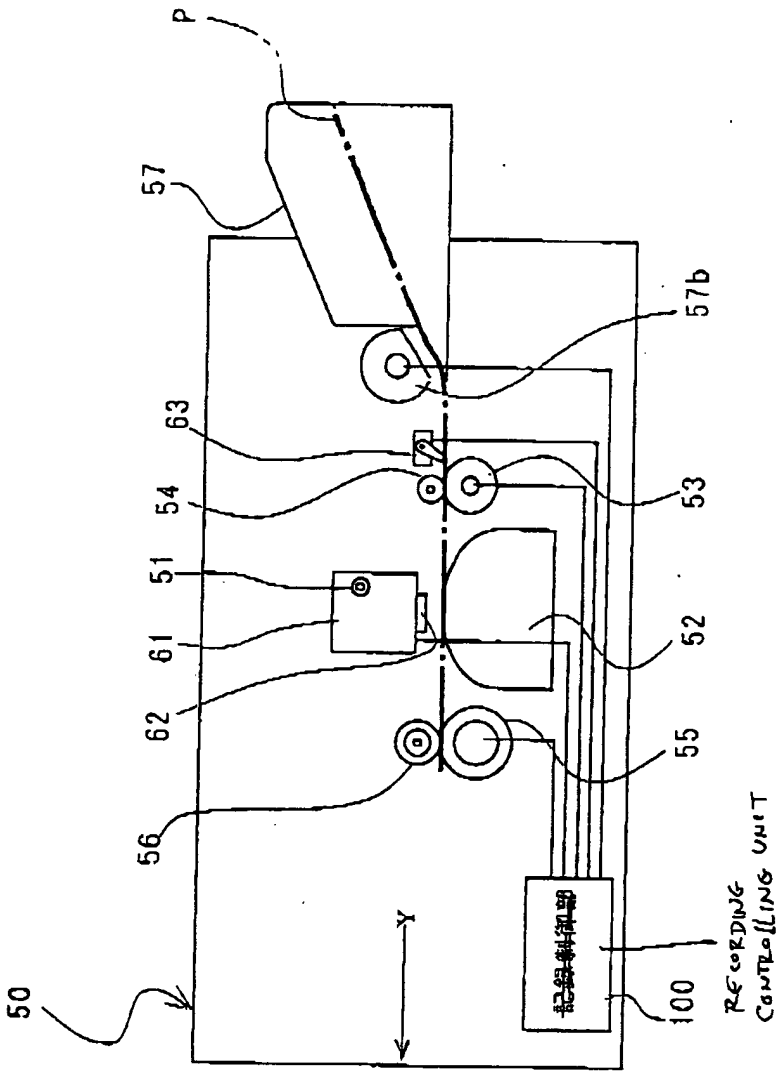


FIG. 3

整理No. F008765
管理No. 099003

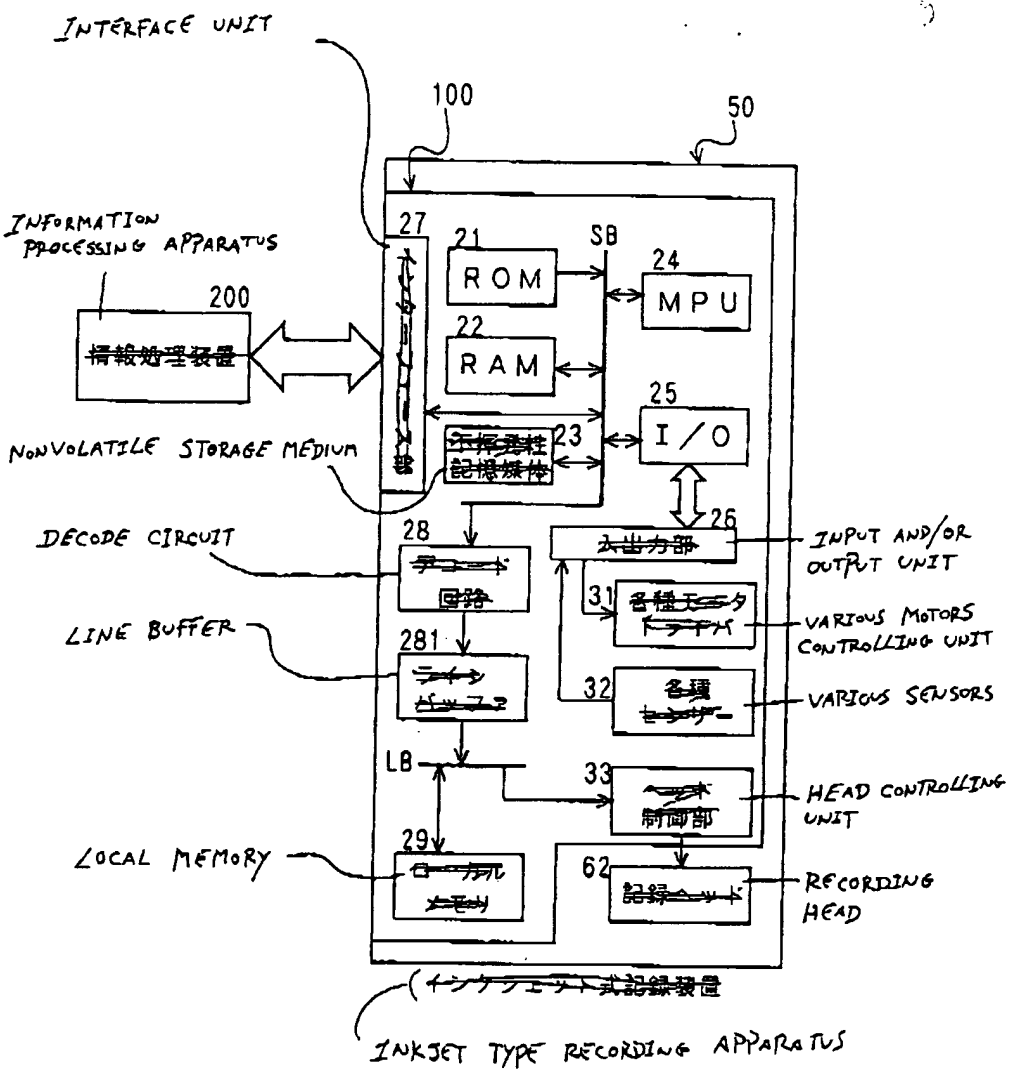


FIG. 4

受付No. 5008765
特許No. EP9003

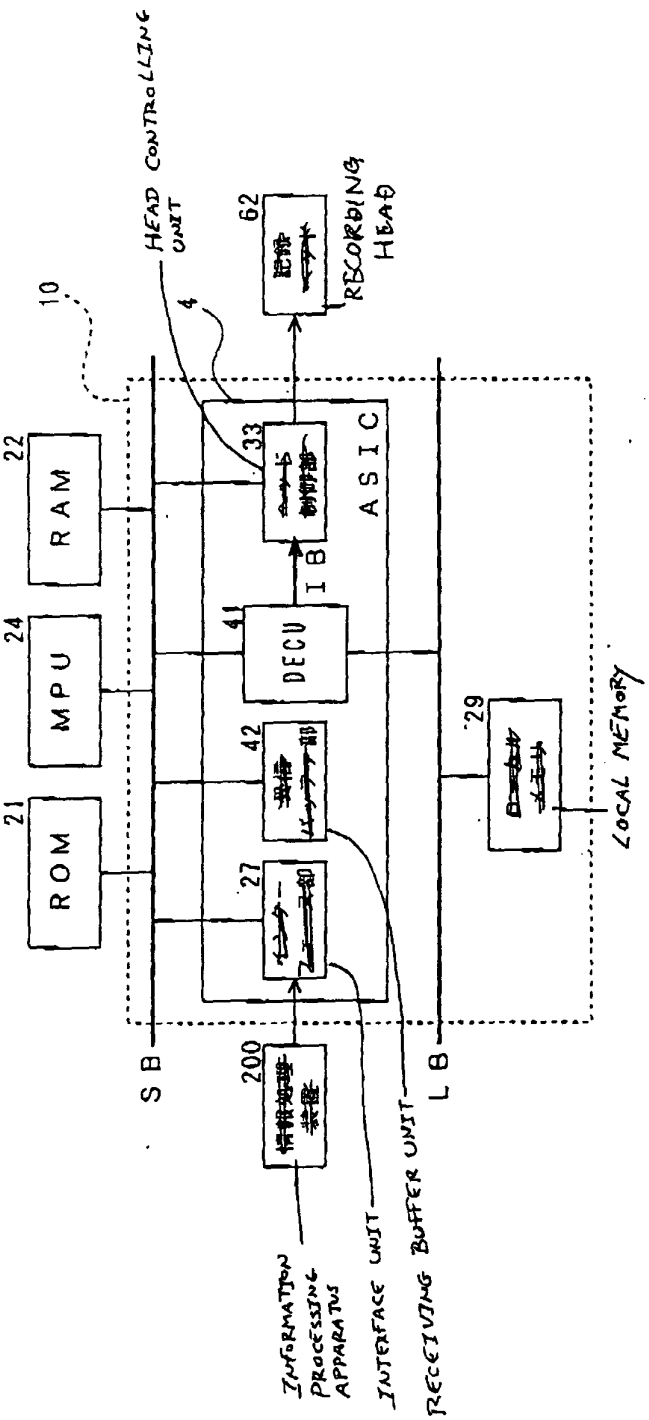


FIG. 5

貴社No. E008765
弊所No. EP9003

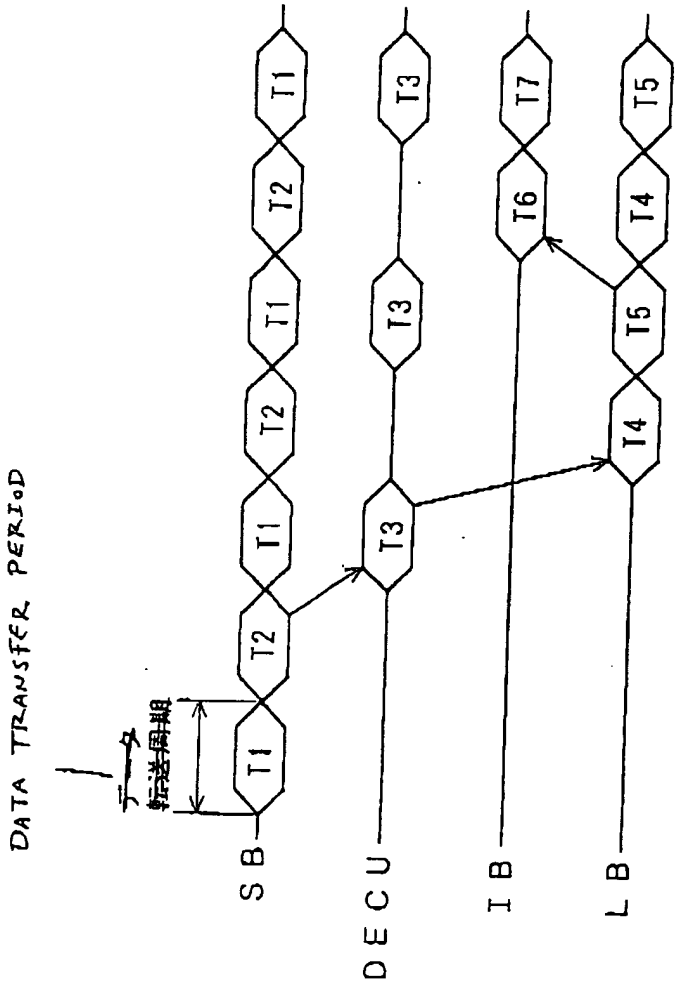
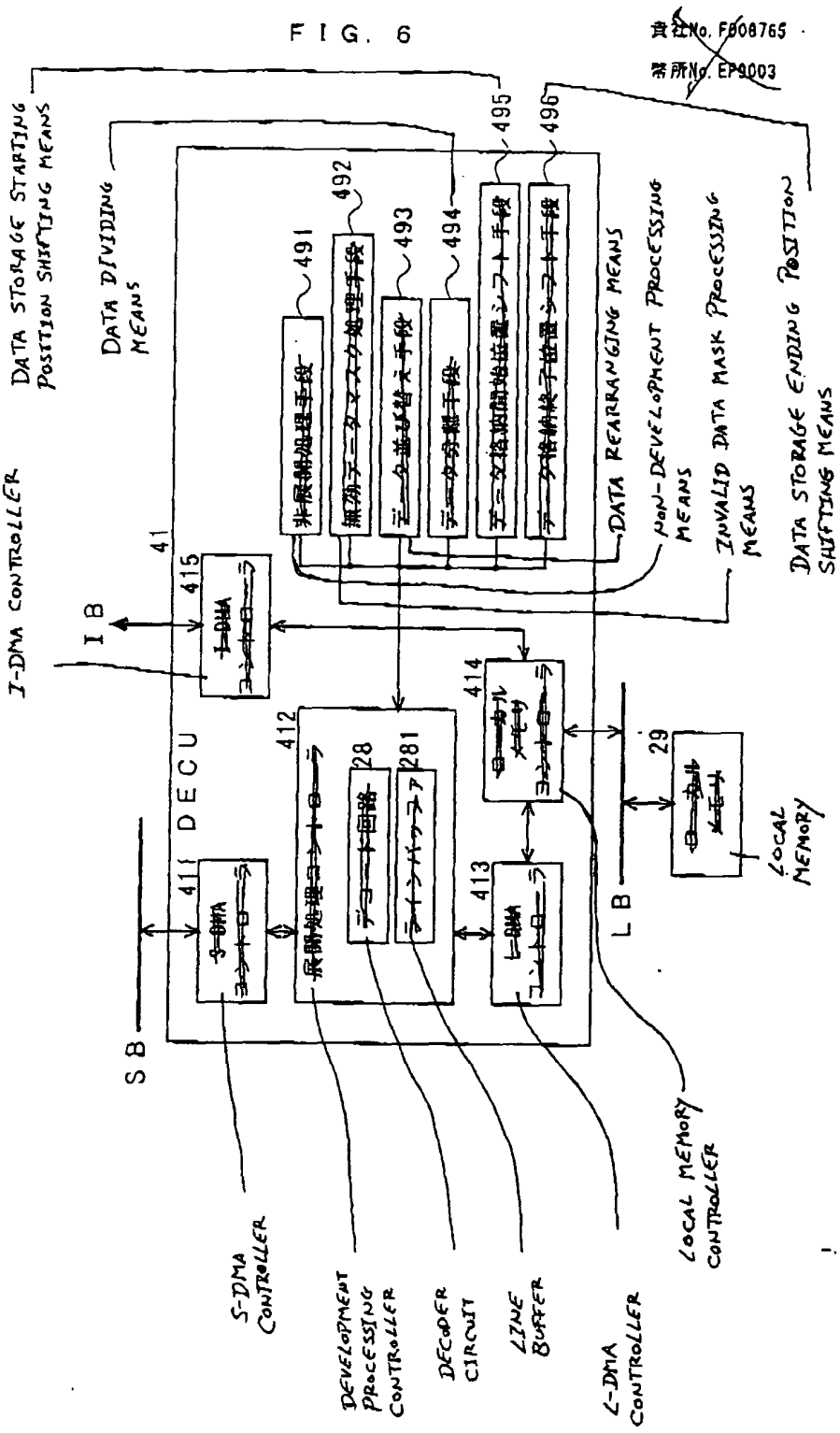


FIG. 6

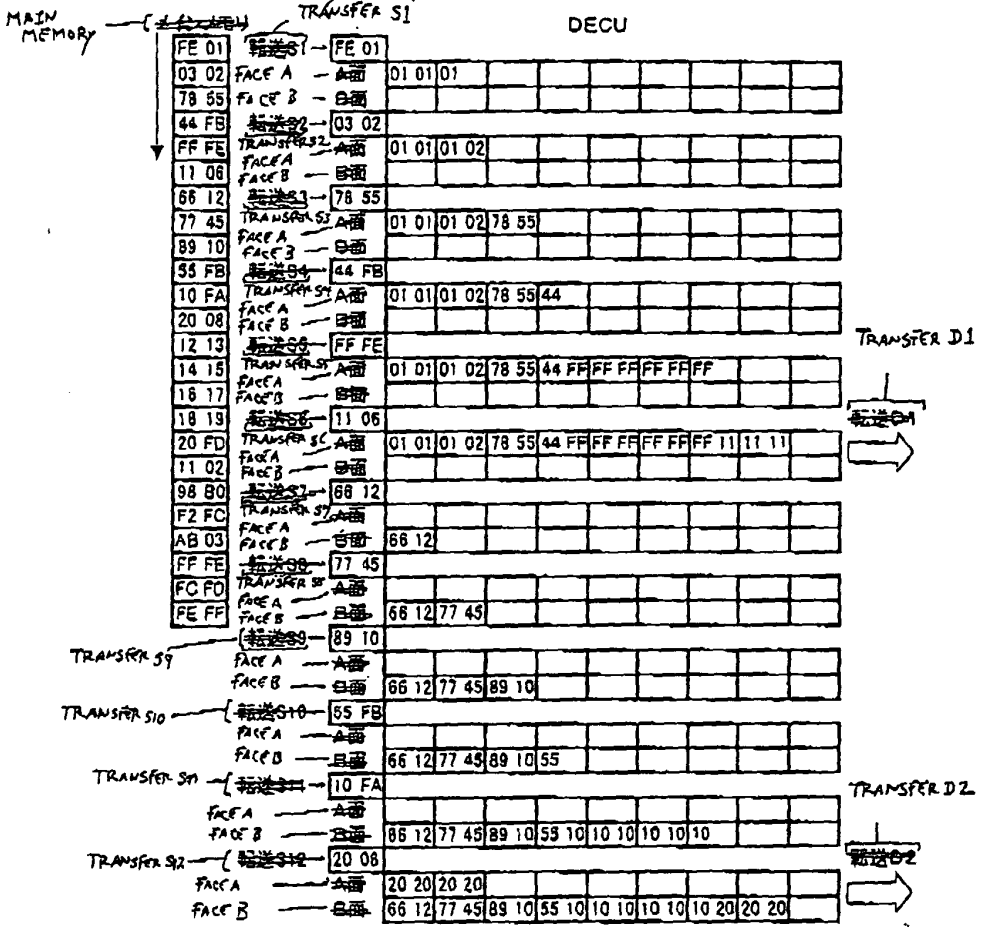

DECU
I-DMA CONTROLLER
DATA STORAGE STARTING POSITION SHIFTING MEANS
DATA DIVIDING MEANS
DATA REARRANGING MEANS
NON-DEVELOPMENT PROCESSING MEANS
INVALID DATA MASK PROCESSING MEANS
DATA STORAGE ENDING POSITION SHIFTING MEANS
I B
L B
S B
41
411
412
413
414
415
28
281
29
491
492
493
494
495
496
S-DMA CONTROLLER
DEVELOPMENT PROCESSING CONTROLLER
DECODER CIRCUIT
LINE BUFFER
L-DMA CONTROLLER
LOCAL MEMORY CONTROLLER
LOCAL MEMORY

貴社No. F008765
弊所No. EP9003

FIG. 7

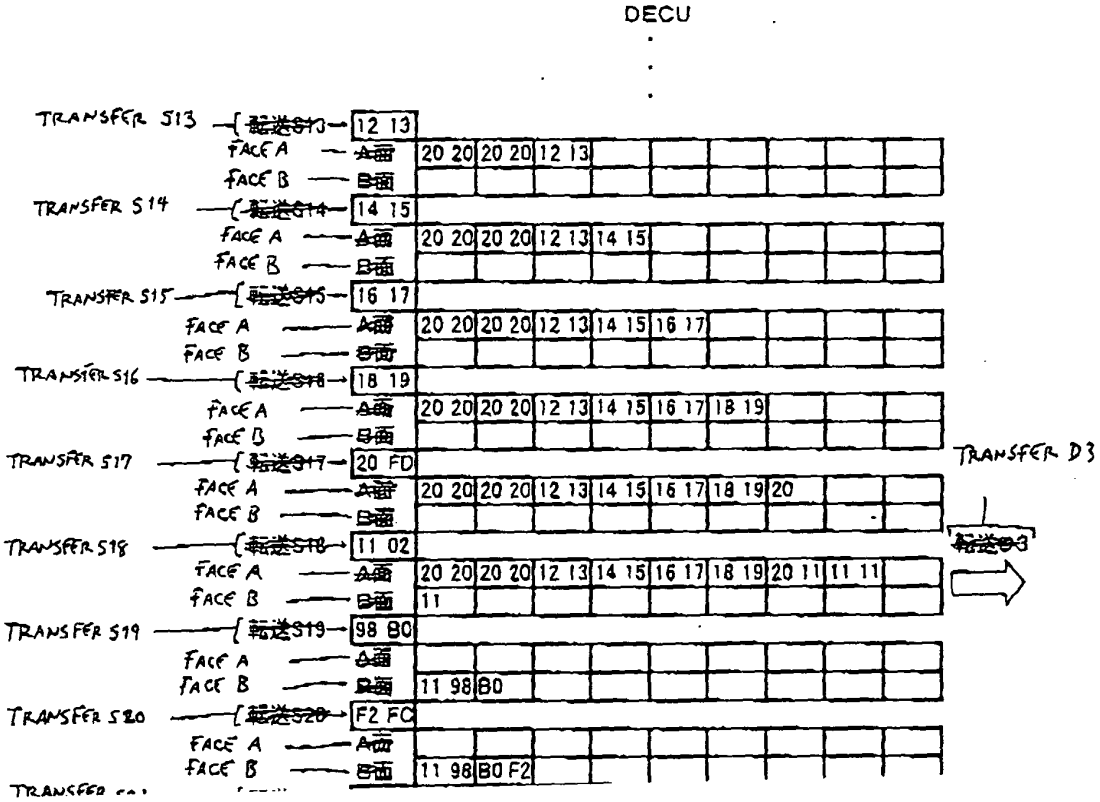
OPERATION CONDITION
MAIN MEMORY SIDE:
STARTING ADDRESS OF RUN LENGTH DATA IS AN EVEN ADDRESS
LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN EVEN ADDRESS
NUMBER OF 1 LINE BUFFER: 16 BYTES

動作条件
メインメモリ側：ランレングスデータの開始アドレス 偶数アドレス
ローカルメモリ側：イメージデータの開始アドレス 偶数アドレス
1ラインバイト数：16バイト



貴社No. F008765
常所No. EP9003

FIG. 8



SETTING CONDITION
NO VERTICAL LINE REARRANGEMENT
TOTAL NUMBER OF DEVELOPED BYTES : 64 BYTES (16×4)
NUMBER OF BYTES IN 1 LINE : 16 BYTES
NUMBER OF DEVELOPED LINES : 4 LINES

~~FIG. 9~~

~~貴社No. FR08765~~
~~弊所No. EP9003~~

設定条件
ライン並び変換なし
総展開バイト数:64バイト(16×4)
1ラインバイト数:16バイト
展開ライン数:4ライン

~~自己加振式~~ → LOCAL MEMORY

~~(a)~~

FIG. 9A

D1→

01 01	01 02	78 55	44 FF
FF FF	FF FF	FF 11	11 11
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00

~~(b)~~

FIG. 9B

01 01	01 02	78 55	44 FF
FF FF	FF FF	FF 11	11 11
D2→ 66 12	77 45	89 10	55 10
10 10	10 10	10 20	20 20
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00

~~(c)~~

FIG. 9C

01 01	01 02	78 55	44 FF
FF FF	FF FF	FF 11	11 11
66 12	77 45	89 10	55 10
10 10	10 10	10 20	20 20
D3→ 20 20	20 20	12 13	14 15
16 17	18 19	20 11	11 11
00 00	00 00	00 00	00 00
00 00	00 00	00 00	00 00

~~(d)~~

FIG. 9D

01 01	01 02	78 55	44 FF
FF FF	FF FF	FF 11	11 11
66 12	77 45	89 10	55 10
10 10	10 10	10 20	20 20
20 20	20 20	12 13	14 15
16 17	18 19	20 11	11 11
D4→ 11 98	B0 F2	AB AB	AB AB
AB FF	FE FC	FD FF	FF FF

貴社No. F008765
弊所No. ER9003

SETTING CONDITION
VERTICAL LINE REARRANGEMENT PERFORMED
TOTAL NUMBER OF DEVELOPED BYTES : 64 BYTES (16×4)
NUMBER of BYTES IN 1 LINE: 16 BYTES
NUMBER OF DEVELOPED LINES: 4 LINES

FIG.10

設定条件
ライン並び変換あり
総展開バイト数:64バイト(16×4)
1ラインバイト数:16バイト
展開ライン数:4ライン

→ LOCAL MEMORY

(a)

FIG.10A

D1↓

01 01	00 00	00 00	00 00	…	00 00
01 02	00 00	00 00	00 00	…	00 00
78 55	00 00	00 00	00 00	…	00 00
44 FF	00 00	00 00	00 00	…	00 00
FF FF	00 00	00 00	00 00	…	00 00
FF FF	00 00	00 00	00 00	…	00 00
FF 11	00 00	00 00	00 00	…	00 00
11 11	00 00	00 00	00 00	…	00 00

(b)

FIG.10B

D2↓

01 01	66 12	00 00	00 00	…	00 00
01 02	77 45	00 00	00 00	…	00 00
78 55	89 10	00 00	00 00	…	00 00
44 FF	55 10	00 00	00 00	…	00 00
FF FF	10 10	00 00	00 00	…	00 00
FF FF	10 10	00 00	00 00	…	00 00
FF 11	10 20	00 00	00 00	…	00 00
11 11	20 20	00 00	00 00	…	00 00

(c)

FIG.10C

D3↓

01 01	66 12	20 20	00 00	…	00 00
01 02	77 45	20 20	00 00	…	00 00
78 55	89 10	12 13	00 00	…	00 00
44 FF	55 10	14 15	00 00	…	00 00
FF FF	10 10	16 17	00 00	…	00 00
FF FF	10 10	18 19	00 00	…	00 00
FF 11	10 20	20 11	00 00	…	00 00
11 11	20 20	11 11	00 00	…	00 00

(d)

FIG.10D

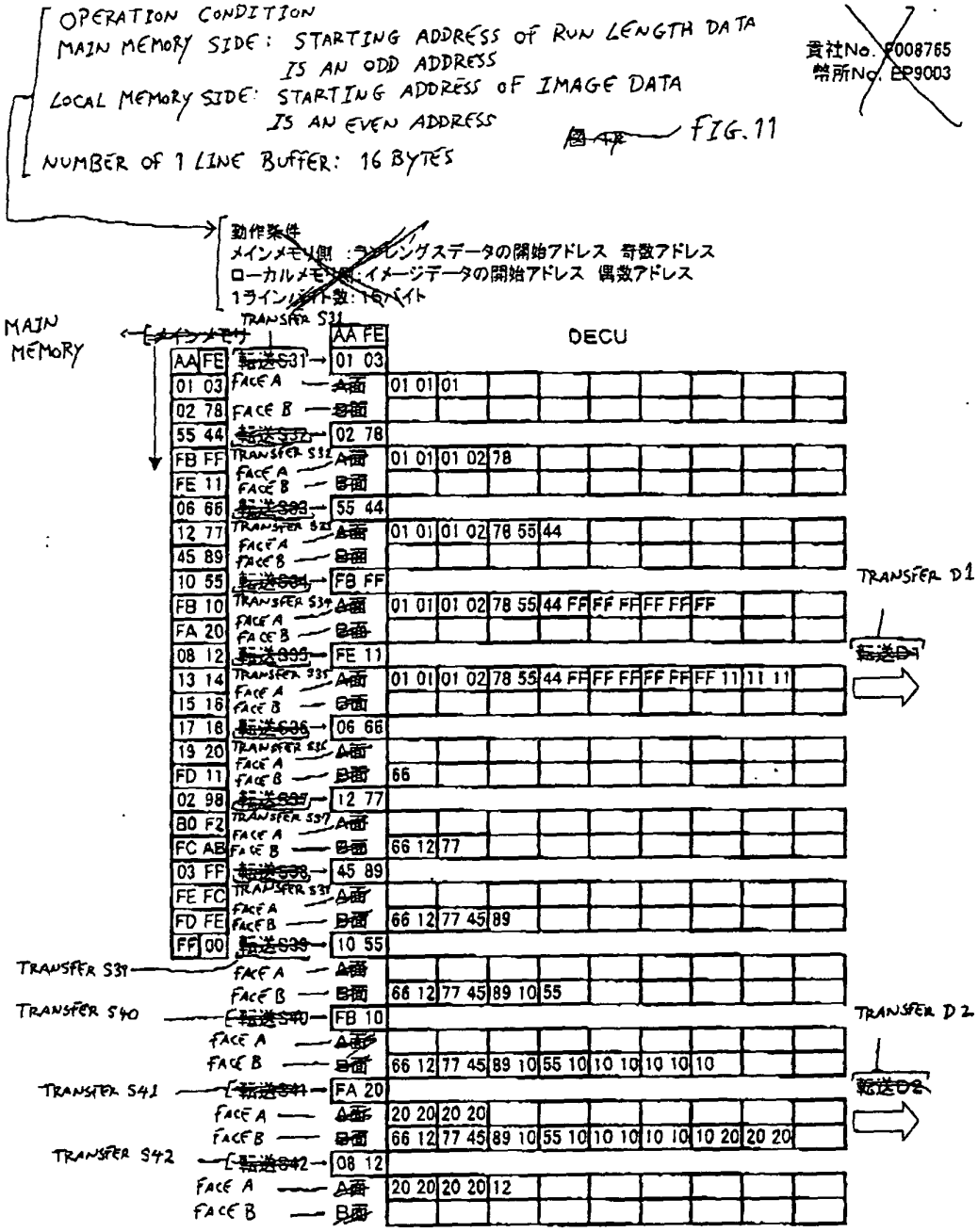
D4↓

01 01	66 12	20 20	11 98	…	00 00
01 02	77 45	20 20	B0 F2	…	00 00
78 55	89 10	12 13	ABAB	…	00 00
44 FF	55 10	14 15	ABAB	…	00 00
FF FF	10 10	16 17	AB FF	…	00 00
FF FF	10 10	18 19	FE FC	…	00 00
FF 11	10 20	20 11	FD FF	…	00 00
11 11	20 20	11 11	FF FF	…	00 00

OPERATION CONDITION
MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN ODD ADDRESS
LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN EVEN ADDRESS
NUMBER OF 1 LINE BUFFER: 16 BYTES

FIG. 11

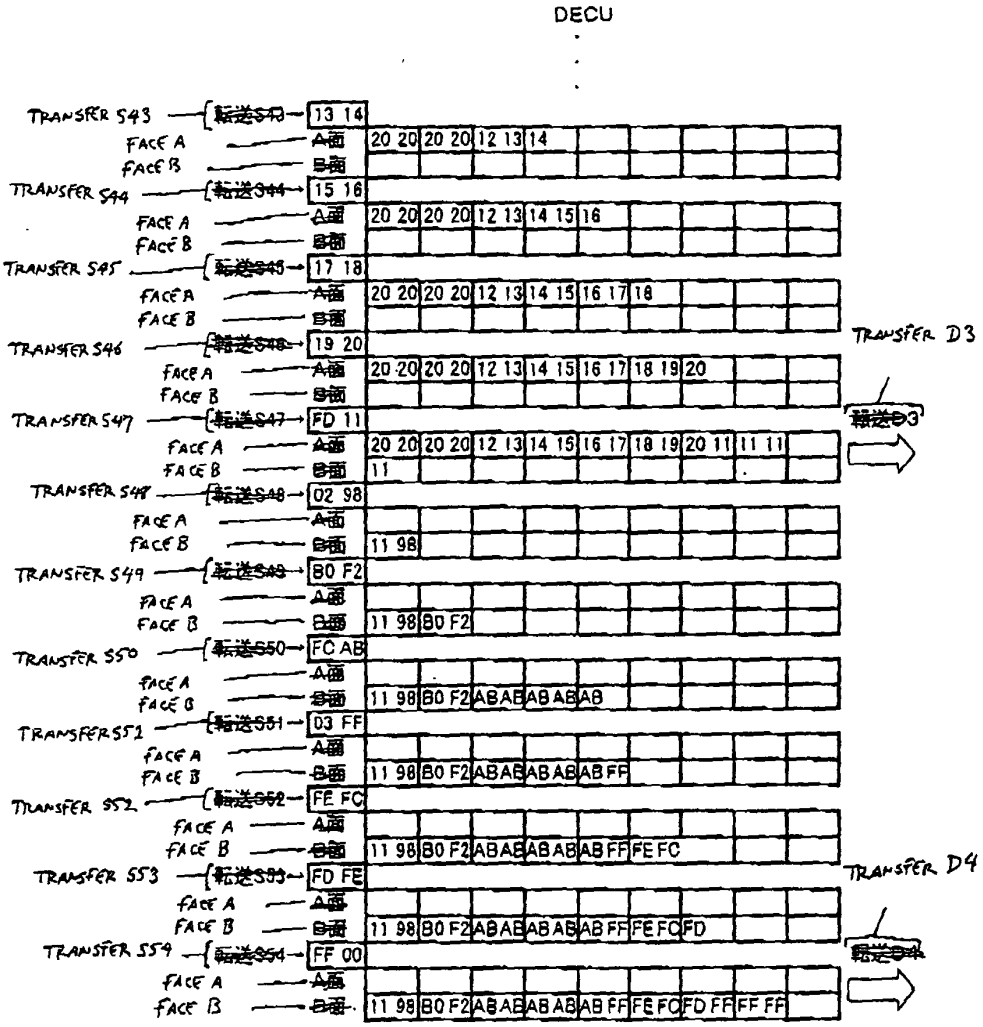
貴社No. P008765
弊所No. EP9003



貴社No. F008765
弊所No. SP9003

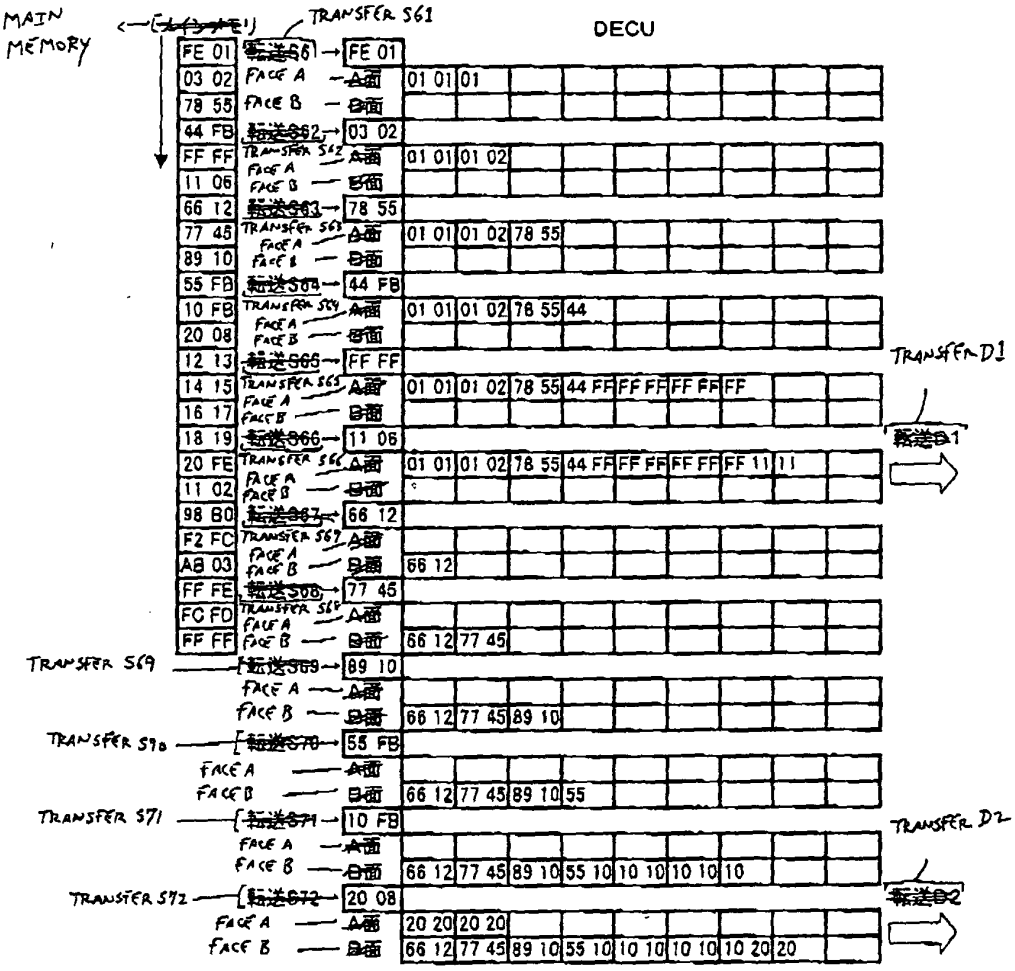
Fig. 12

図12



OPERATION CONDITION
MAIN MEMORY SIDE: STARTING ADDRESS of RUN LENGTH DATA IS AN EVEN ADDRESS
LOCAL MEMORY SIDE: STARTING ADDRESS of IMAGE DATA IS AN EVEN ADDRESS
NUMBER of 1 LINE BUFFER : 15 BYTES 図6 FIG.13

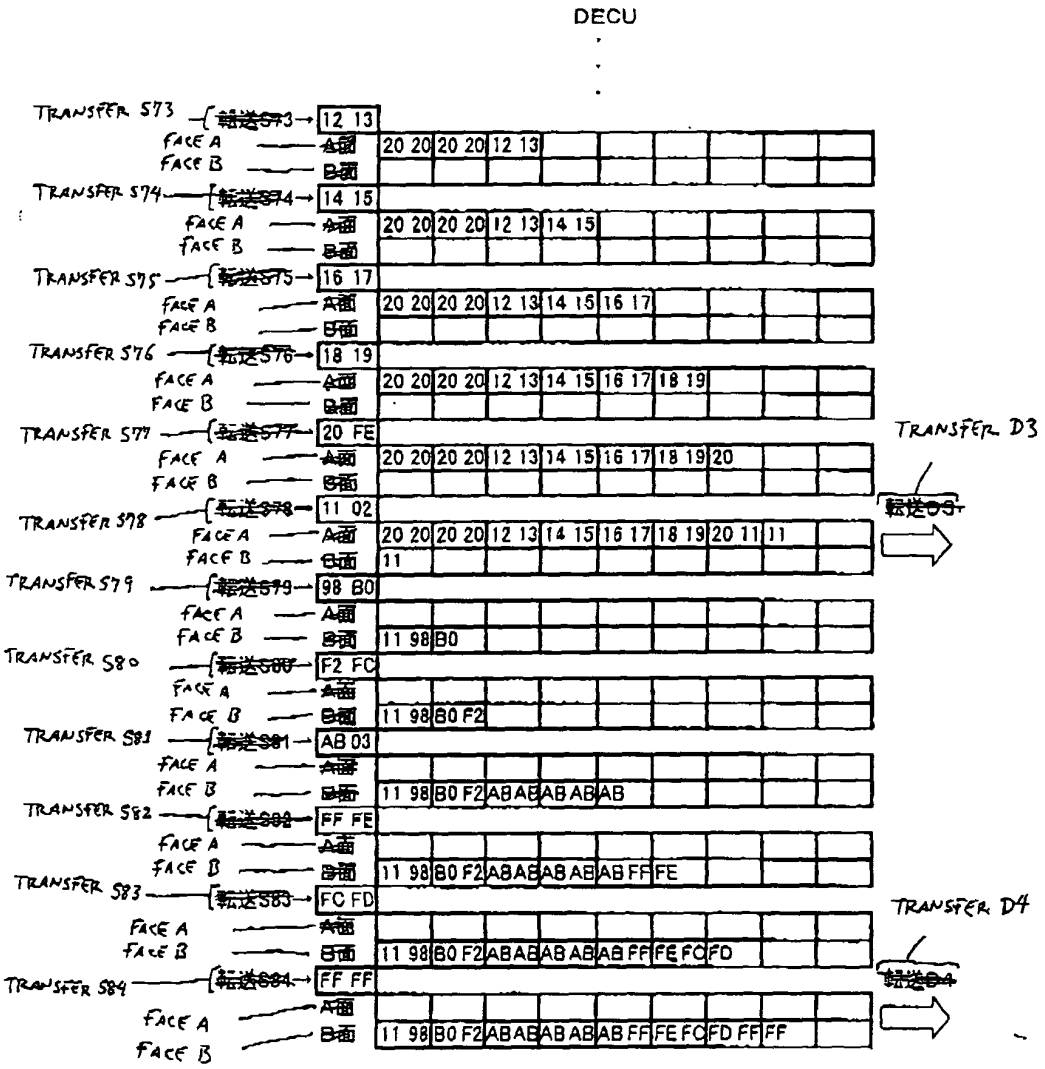
~~動作条件~~
~~メインメモリ側：ランレングスデータの開始アドレス/偶数アドレス~~
~~ローカルメモリ側：イメージデータの開始アドレス/偶数アドレス~~
~~ラインバイト数：15バイト~~



貴社No. F008765
弊所No. EP9003

FIG. 14

図14



SETTING CONDITION
VERTICAL LINE REARRANGEMENT PERFORMED
TOTAL NUMBER of DEVELOPED BYTES : 60 BYTES (15×4)
NUMBER of BYTES IN 1 LINE : 15 BYTES
NUMBER of DEVELOPED LINES : 4 LINES

~~図 15~~

~~会社No. F008765~~
~~整所No. EP9003~~

~~設定条件~~
~~ライン様並び変換あり~~
~~総展開バイト数:60バイト(15×4)~~
~~1ラインバイト数:15バイト~~
~~展開ライン数:4ライン~~

~~ローカルメモリ~~ → LOCAL MEMORY

~~(a)~~

FIG. 15A

D1↓

01	01	00	00	00	00	00	00	…	00	00				
01	02	00	00	00	00	00	00	…	00	00				
78	55	00	00	00	00	00	00	…	00	00				
44	FF	00	00	00	00	00	00	…	00	00				
FF	FF	00	00	00	00	00	00	…	00	00				
FF	FF	00	00	00	00	00	00	…	00	00				
FF	11	00	00	00	00	00	00	…	00	00				
11	00	00	00	00	00	00	00	…	00	00				

~~(b)~~

FIG. 15B

D2↓

01	01	66	12	00	00	00	00	…	00	00				
01	02	77	45	00	00	00	00	…	00	00				
78	55	89	10	00	00	00	00	…	00	00				
44	FF	55	10	00	00	00	00	…	00	00				
FF	FF	10	10	00	00	00	00	…	00	00				
FF	FF	10	10	00	00	00	00	…	00	00				
FF	11	10	20	00	00	00	00	…	00	00				
11	00	20	00	00	00	00	00	…	00	00				

~~(c)~~

FIG. 15C

D3↓

01	01	66	12	20	20	00	00	…	00	00				
01	02	77	45	20	20	00	00	…	00	00				
78	55	89	10	12	13	00	00	…	00	00				
44	FF	55	10	14	15	00	00	…	00	00				
FF	FF	10	10	16	17	00	00	…	00	00				
FF	FF	10	10	18	19	00	00	…	00	00				
FF	11	10	20	20	11	00	00	…	00	00				
11	00	20	00	11	00	00	00	…	00	00				

~~(d)~~

FIG. 15D

D4↓

01	01	66	12	20	20	11	98	…	00	00				
01	02	77	45	20	20	B0	F2	…	00	00				
78	55	89	10	12	13	AB	AB	…	00	00				
44	FF	55	10	14	15	AB	AB	…	00	00				
FF	FF	10	10	16	17	AB	FF	…	00	00				
FF	FF	10	10	18	19	FE	FC	…	00	00				
FF	11	10	20	20	11	FD	FF	…	00	00				
11	00	20	00	11	00	FF	00	…	00	00				

会社No. F008765
管所No. EP9003

図16

設定条件
ライン縦並び変換なし
総展開バイト数:60バイト(15×4)
1ラインバイト数:15バイト
展開ライン数:4ライン

SETTING CONDITION
NO VERTICAL LINE REARRANGEMENT
TOTAL NUMBER OF DEVELOPED BYTES: 60 BYTES (15X4)
NUMBER OF BYTES IN 1 LINE: 15 BYTES
NUMBER OF DEVELOPED LINES: 4 LINES

ローカルメモリ
LOCAL MEMORY

(a) D1→

01 01	01 02	78 55	44 FF		
FF FF	FF FF	FF 11	11	00	
00 00	00 00	00 00	00 00		
00 00	00 00	00 00	00 00		
00 00	00 00	00 00	00 00		
00 00	00 00	00 00	00 00		
00 00	00 00	00 00	00 00		
00 00	00 00	00 00	00 00		

FIG. 16A

(b) D2→

01 01	01 02	78 55	44 FF		
FF FF	FF FF	FF 11	11	00	
66 12	77 45	89 10	55 10		
10 10	10 10	10 20	20	00	
00 00	00 00	00 00	00 00		
00 00	00 00	00 00	00 00		
00 00	00 00	00 00	00 00		
00 00	00 00	00 00	00 00		

FIG. 16B

(c) D3→

01 01	01 02	78 55	44 FF		
FF FF	FF FF	FF 11	11	00	
66 12	77 45	89 10	55 10		
10 10	10 10	10 20	20	00	
20 20	20 20	12 13	14 15		
16 17	18 19	20 11	11	00	
00 00	00 00	00 00	00 00		
00 00	00 00	00 00	00 00		

FIG. 16C

(d) D4→

01 01	01 02	78 55	44 FF		
FF FF	FF FF	FF 11	11	00	
66 12	77 45	89 10	55 10		
10 10	10 10	10 20	20	00	
20 20	20 20	12 13	14 15		
16 17	18 19	20 11	11	00	
11 98	B0 F2	AB AB	AB AB		
AB FF	FE FC	FD FF	FF	00	

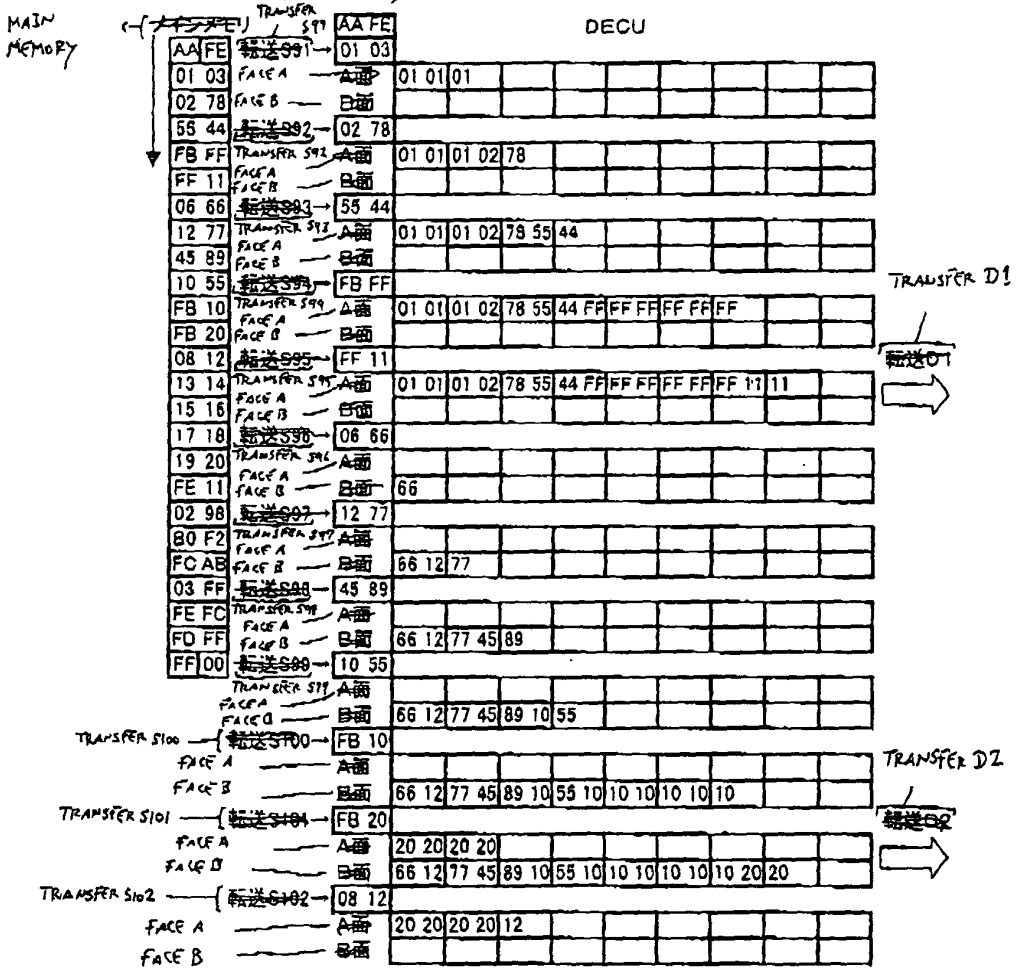
FIG. 16D

FIG. 17

OPERATION CONDITION
MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN ODD ADDRESS
LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN EVEN ADDRESS
NUMBER of 1 LINE BUFFER: 15 BYTES

貴社No. F008765
弊所No. EP9003

動作条件
メインメモリ側：ランレングスデータの開始アドレス 奇数アドレス
ローカルメモリ側：イメージデータの開始アドレス 偶数アドレス
1ラインバイト数：15バイト

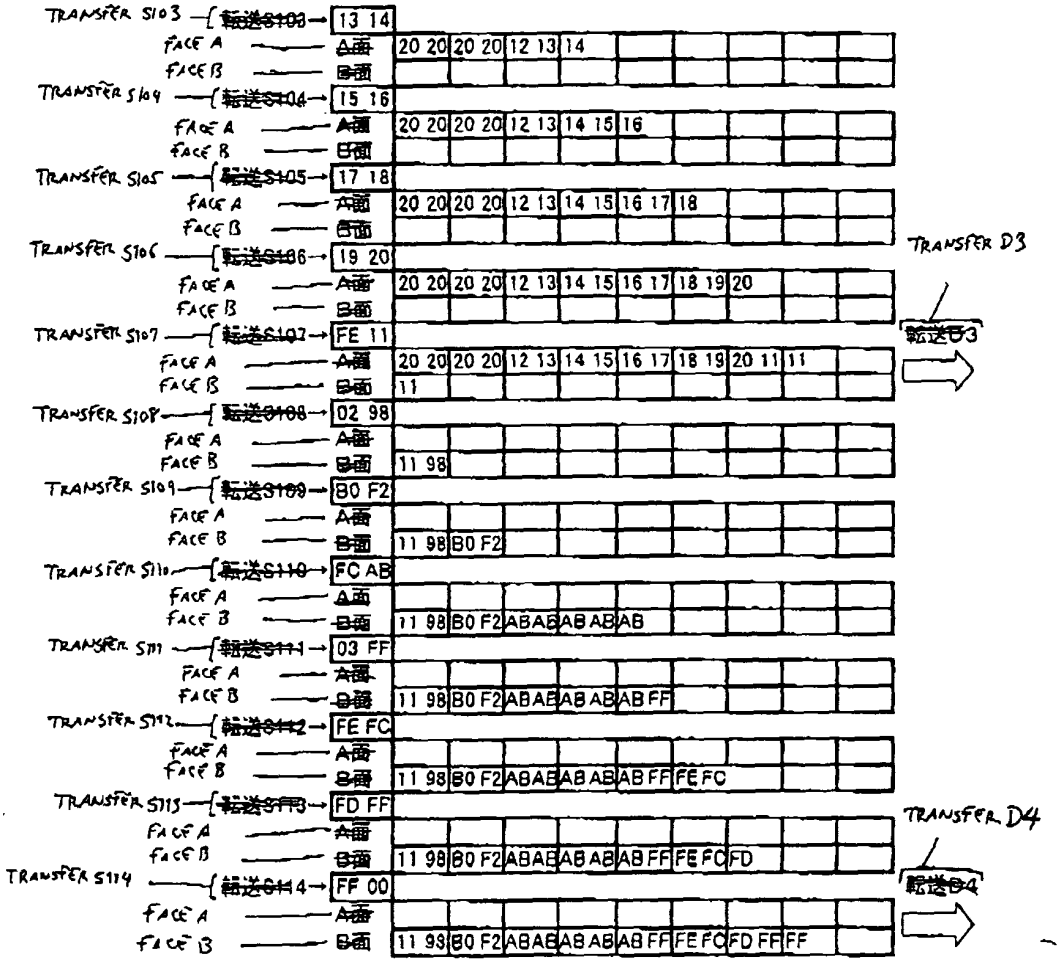


貴社No. F008765
管所No. EP9003

FIG.18

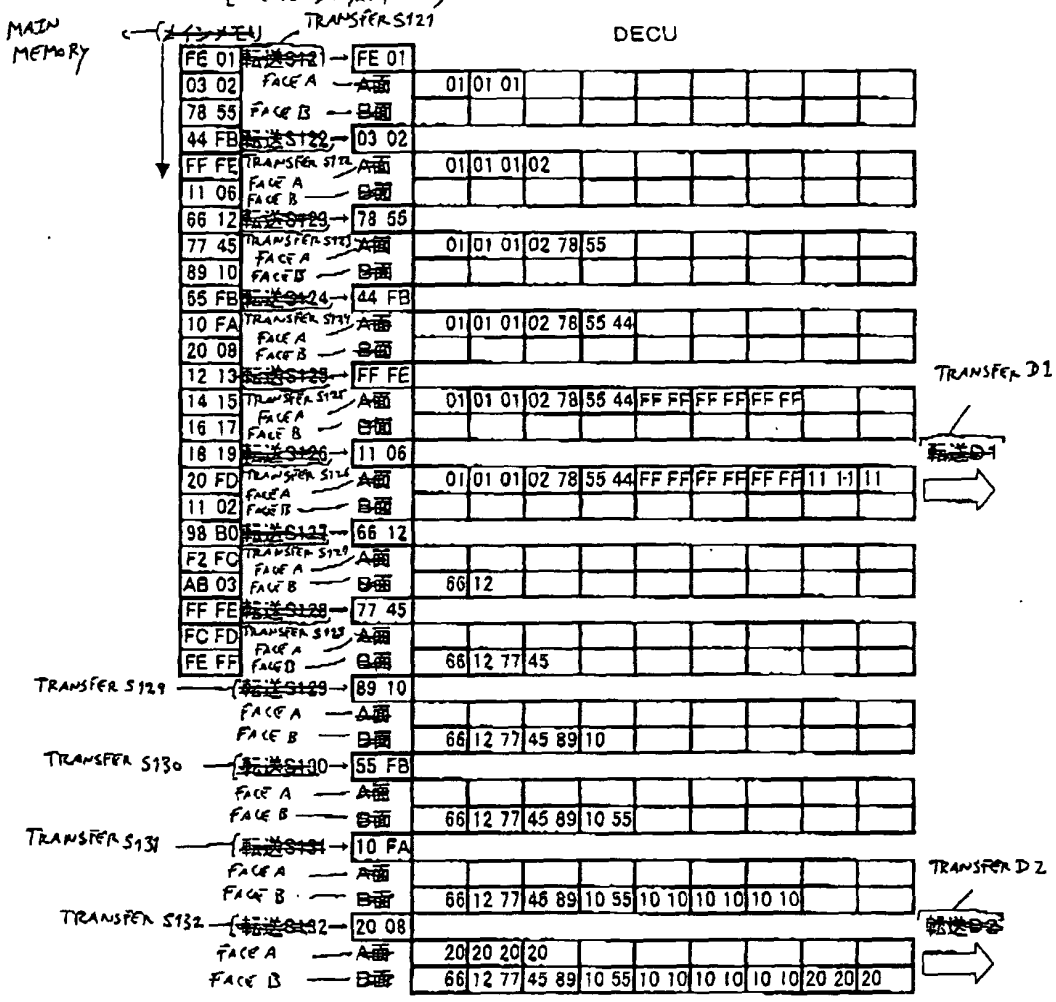
図 18

DECU



OPERATION CONDITION
MAIN MEMORY SIDE: STARTING ADDRESS of RUN LENGTH DATA IS AN EVEN ADDRESS
LOCAL MEMORY SIDE: STARTING ADDRESS of IMAGE DATA IS AN ODD ADDRESS
NUMBER of 1 LINE BUFFER: 16 BYTES

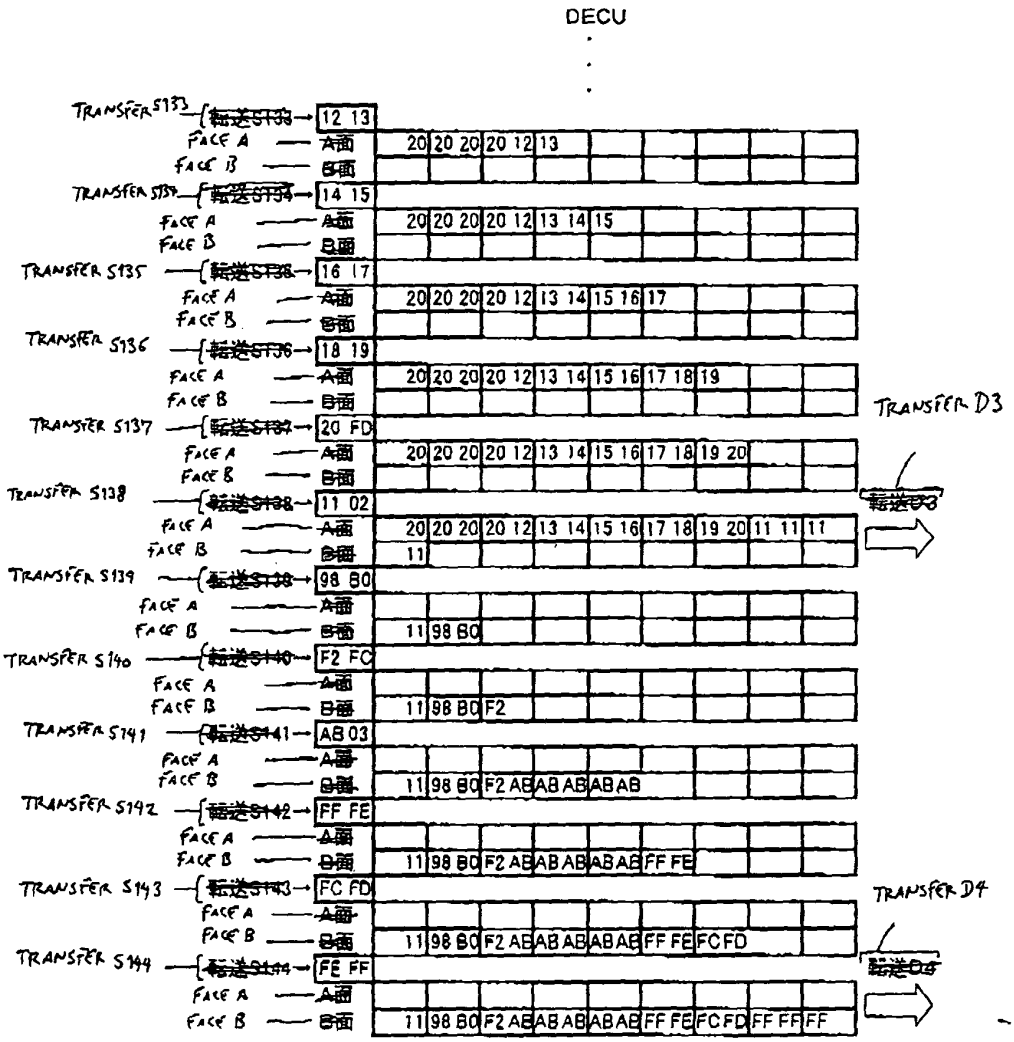
FIG. 19



貴社No. F008765
弊所No. EP9003

FIG. 20

図 20



SETTING CONDITION
VERTICAL LINE REARRANGEMENT PERFORMED
TOTAL NUMBER of DEVELOPED BYTES: 64 BYTES (16×4)
NUMBER OF BYTES IN 1 LINE: 16 BYTES
NUMBER OF DEVELOPED LINES: 4 LINES

~~図 21~~

~~出願人~~ 弁理士 社No. F008765
~~代理人~~ 弁理士 所No. EP9003

設定条件
ライン順並び変換あり
総展開バイト数:64バイト(16×4)
1ラインバイト数:16バイト
展開ライン数:4ライン

~~ローカルメモリ~~ LOCAL MEMORY

FIG. 21A

(a)

D1↓

00	01	00	00	00	00	00	00	…	00	00	00	00	00	00	00	00
01	01	00	00	00	00	00	00	…	00	00	00	00	00	00	00	00
02	78	00	00	00	00	00	00	…	00	00	00	00	00	00	00	00
55	44	00	00	00	00	00	00	…	00	00	00	00	00	00	00	00
FF	FF	00	00	00	00	00	00	…	00	00	00	00	00	00	00	00
FF	FF	00	00	00	00	00	00	…	00	00	00	00	00	00	00	00
FF	FF	00	00	00	00	00	00	…	00	00	00	00	00	00	00	00
11	11	00	00	00	00	00	00	…	00	00	00	00	00	00	00	00
11	00	00	00	00	00	00	00	…	00	00	00	00	00	00	00	00

FIG. 21B

(b)

D2↓

00	01	00	66	00	00	00	00	…	00	00	00	00	00	00	00	00
01	01	12	77	00	00	00	00	…	00	00	00	00	00	00	00	00
02	78	45	89	00	00	00	00	…	00	00	00	00	00	00	00	00
55	44	10	55	00	00	00	00	…	00	00	00	00	00	00	00	00
FF	FF	10	10	00	00	00	00	…	00	00	00	00	00	00	00	00
FF	FF	10	10	00	00	00	00	…	00	00	00	00	00	00	00	00
FF	FF	10	10	00	00	00	00	…	00	00	00	00	00	00	00	00
11	11	20	20	00	00	00	00	…	00	00	00	00	00	00	00	00
11	00	20	00	00	00	00	00	…	00	00	00	00	00	00	00	00

FIG. 21C

(c)

D3↓

00	01	00	66	00	20	00	00	…	00	00	00	00	00	00	00	00
01	01	12	77	20	20	00	00	…	00	00	00	00	00	00	00	00
02	78	45	89	20	12	00	00	…	00	00	00	00	00	00	00	00
55	44	10	55	13	14	00	00	…	00	00	00	00	00	00	00	00
FF	FF	10	10	15	16	00	00	…	00	00	00	00	00	00	00	00
FF	FF	10	10	17	18	00	00	…	00	00	00	00	00	00	00	00
FF	FF	10	10	19	20	00	00	…	00	00	00	00	00	00	00	00
11	11	20	20	11	11	00	00	…	00	00	00	00	00	00	00	00
11	00	20	00	11	00	00	00	…	00	00	00	00	00	00	00	00

FIG. 21D

(d)

D4↓

00	01	00	66	00	20	00	11	…	00	00	00	00	00	00	00	00
01	01	12	77	20	20	98	B0	…	00	00	00	00	00	00	00	00
02	78	45	89	20	12	F2	AE	…	00	00	00	00	00	00	00	00
55	44	10	55	13	14	AB	AB	…	00	00	00	00	00	00	00	00
FF	FF	10	10	15	16	AB	AB	…	00	00	00	00	00	00	00	00
FF	FF	10	10	17	18	FF	FE	…	00	00	00	00	00	00	00	00
FF	FF	10	10	19	20	FC	FD	…	00	00	00	00	00	00	00	00
11	11	20	20	11	11	FF	FF	…	00	00	00	00	00	00	00	00
11	00	20	00	11	00	FF	00	…	00	00	00	00	00	00	00	00

OPERATION CONDITION
MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN EVEN ADDRESS
LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN ODD ADDRESS
NUMBER of 1 LINE BUFFER: 15 BYTES

FIG. 22

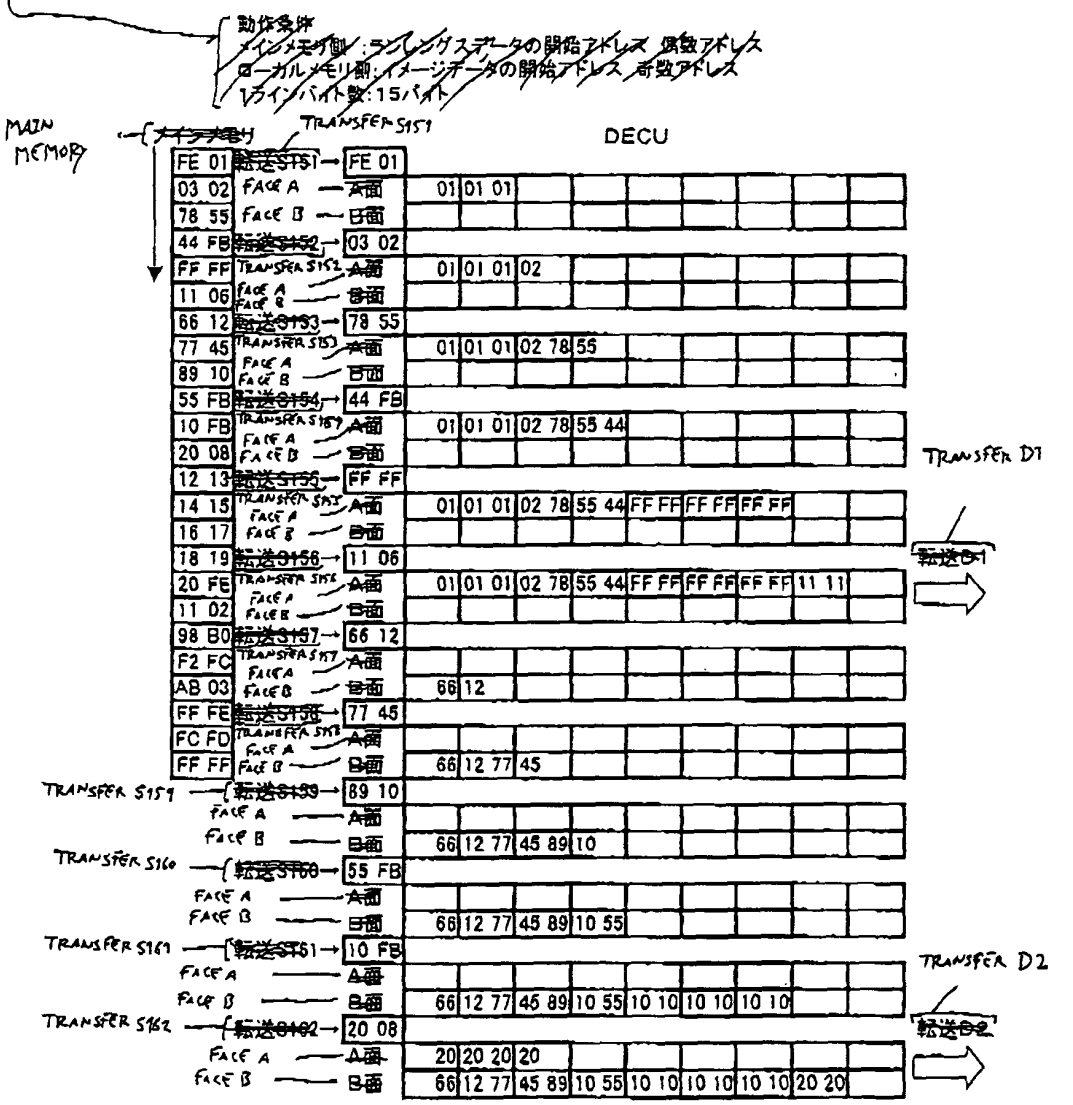
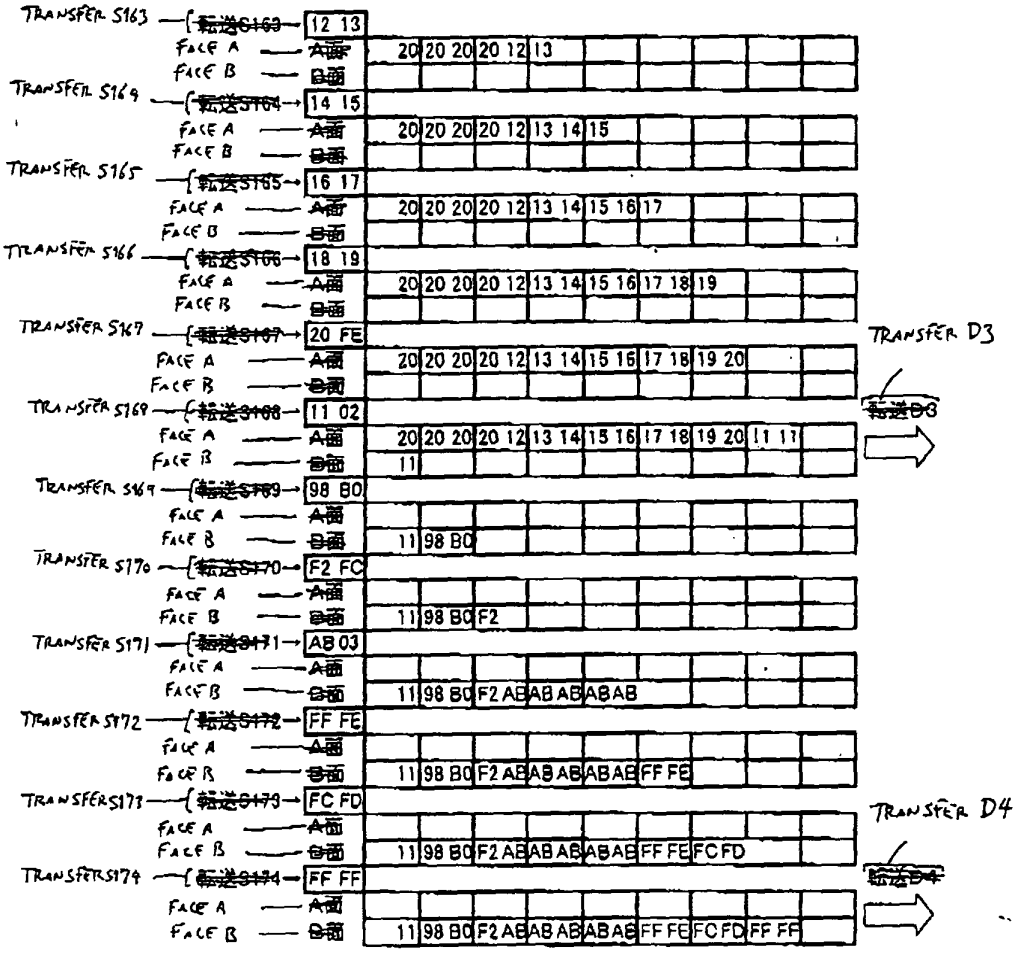


FIG. 23

図23

貴社No. 5008765
弊所No. SP9003

DECU



SETTING CONDITION.
VERTICAL LINE REARRANGEMENT PERFORMED
TOTAL NUMBER of DEVELOPED BYTES: 60 BYTES (15X4)
NUMBER of BYTES IN 1 LINE: 15 BYTES
NUMBER OF DEVELOPED LINES: 4 LINES

受付No. 700876S
整理No. 6P9003

設定条件
ライン縦並び変換あり
総展開バイト数:60バイト(15×4)
1ラインバイト数:15バイト
展開ライン数:4ライン

LOCAL MEMORY

FIG. 24A

D1↓

00	01	00	00	00	00	00	00	00 00
01	01	00	00	00	00	00	00	00 00
02	78	00	00	00	00	00	00	00 00
55	44	00	00	00	00	00	00	00 00
FF	FF	00	00	00	00	00	00	00 00
FF	FF	00	00	00	00	00	00	00 00
FF	FF	00	00	00	00	00	00	00 00
11	11	00	00	00	00	00	00	00 00

FIG. 24B

D2↓

00	01	00	66	00	00	00	00	00 00
01	01	12	77	00	00	00	00	00 00
02	78	45	89	00	00	00	00	00 00
55	44	10	55	00	00	00	00	00 00
FF	FF	10	10	00	00	00	00	00 00
FF	FF	10	10	00	00	00	00	00 00
FF	FF	10	10	00	00	00	00	00 00
11	11	20	20	00	00	00	00	00 00

FIG. 24C

D3↓

00	01	00	66	00	20	00	00	00 00
01	01	12	77	20	20	00	00	00 00
02	78	45	89	20	12	00	00	00 00
55	44	10	55	13	14	00	00	00 00
FF	FF	10	10	15	16	00	00	00 00
FF	FF	10	10	17	18	00	00	00 00
FF	FF	10	10	19	20	00	00	00 00
11	11	20	20	11	11	00	00	00 00

FIG. 24D

D4↓

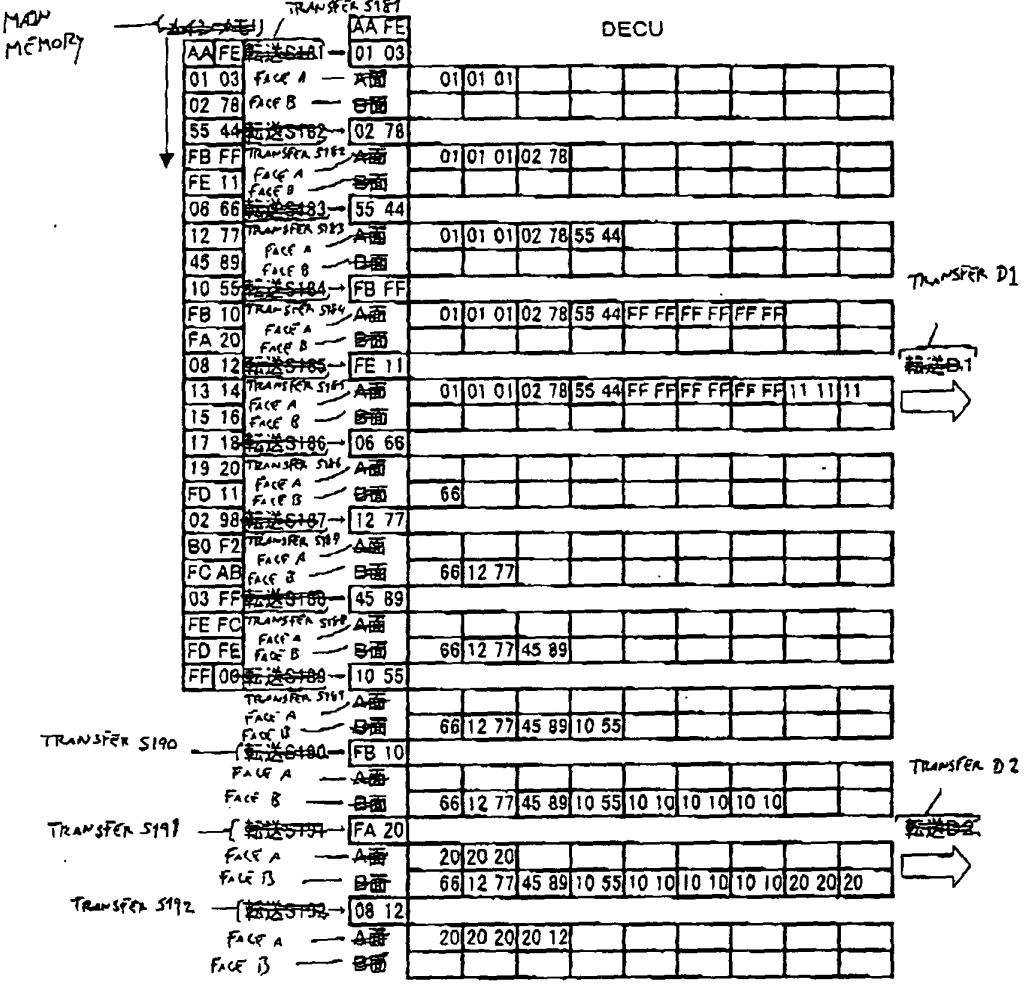
00	01	00	66	00	20	00	11	00 00
01	01	12	77	20	20	98	B0	00 00
02	78	45	89	20	12	F2	AB	00 00
55	44	10	55	13	14	AB	AB	00 00
FF	FF	10	10	15	16	AB	AB	00 00
FF	FF	10	10	17	18	FF	FE	00 00
FF	FF	10	10	19	20	FC	FD	00 00
11	11	20	20	11	11	FF	FF	00 00

OPERATION CONDITION
MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN ODD ADDRESS
LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN ODD ADDRESS
NUMBER OF 1 LINE BUFFER: 16 BYTES

~~図 25~~ FIG. 25

会社No. F008765
管所No. EP9003

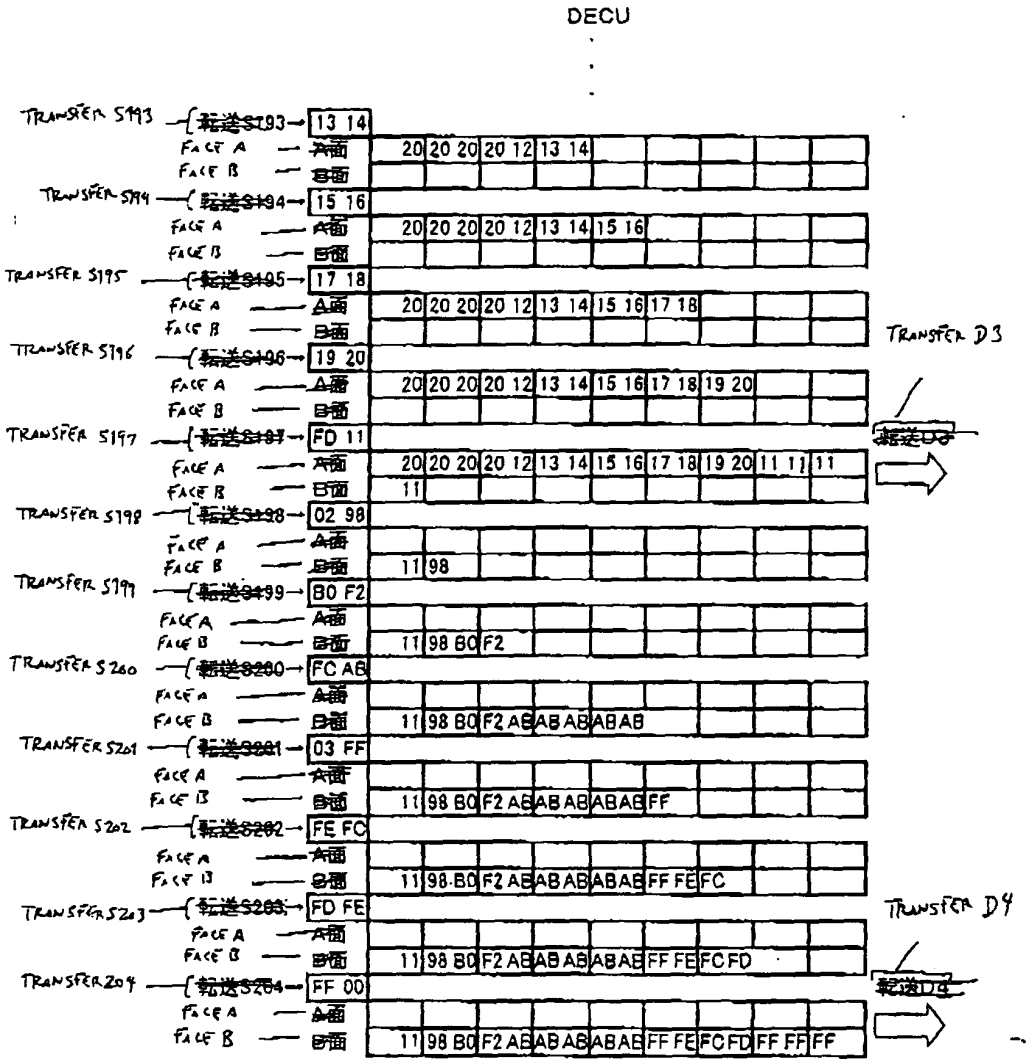
動作条件
メインメモリ側：ランレングスデータの開始アドレス 奇数アドレス
ローカルメモリ側：イメージデータの開始アドレス 奇数アドレス
ライン バイト数：16バイト



貴社No. F008765
弊所No. EP9003

FIG. 26

図 26



OPERATION CONDITION
MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN ODD ADDRESS
LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN ODD ADDRESS
NUMBER OF 1 LINE BUFFER: 15 BYTES FIG. 27

貴社No. F008765
当所No. EP9003

動作条件
メインメモリ側：ランレングスデータの開始アドレス 奇数アドレス
ローカルメモリ側：イメージデータの開始アドレス 奇数アドレス
1ラインバイト数：15バイト

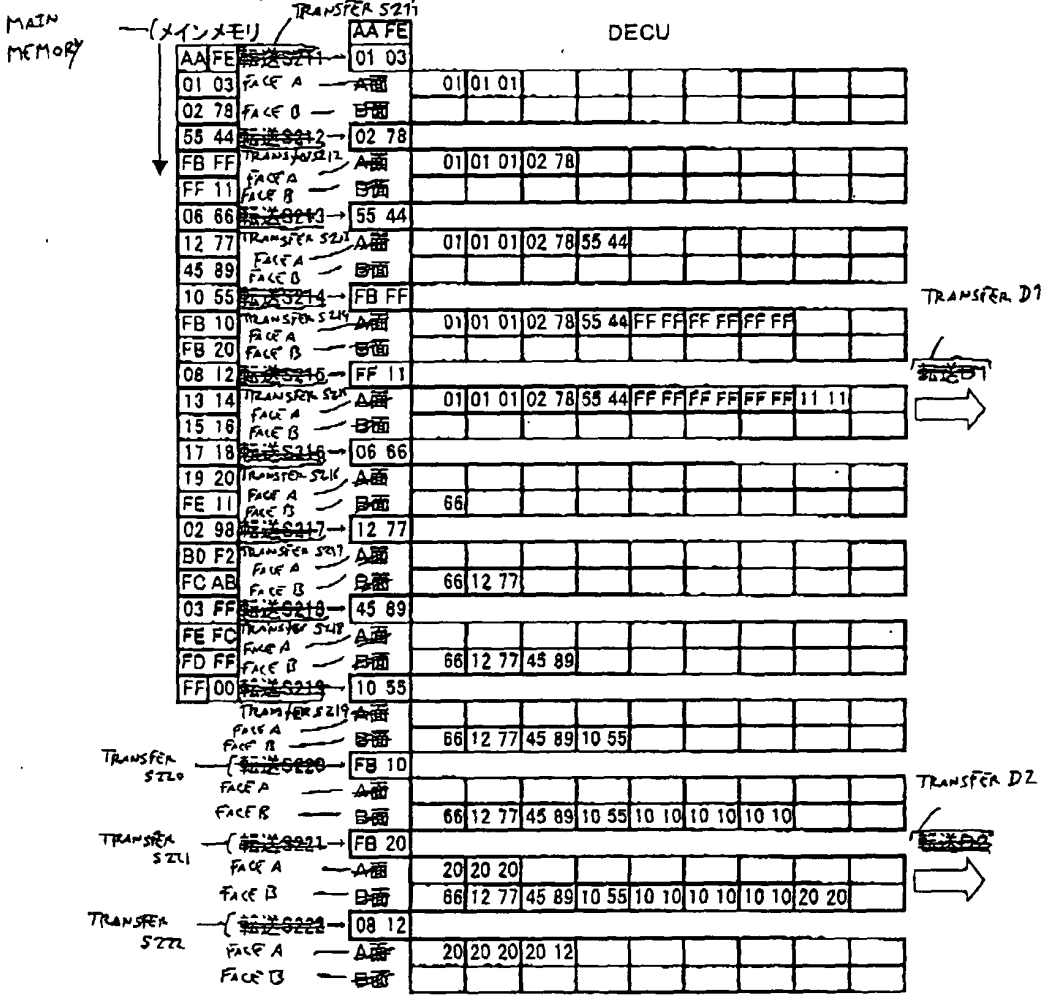
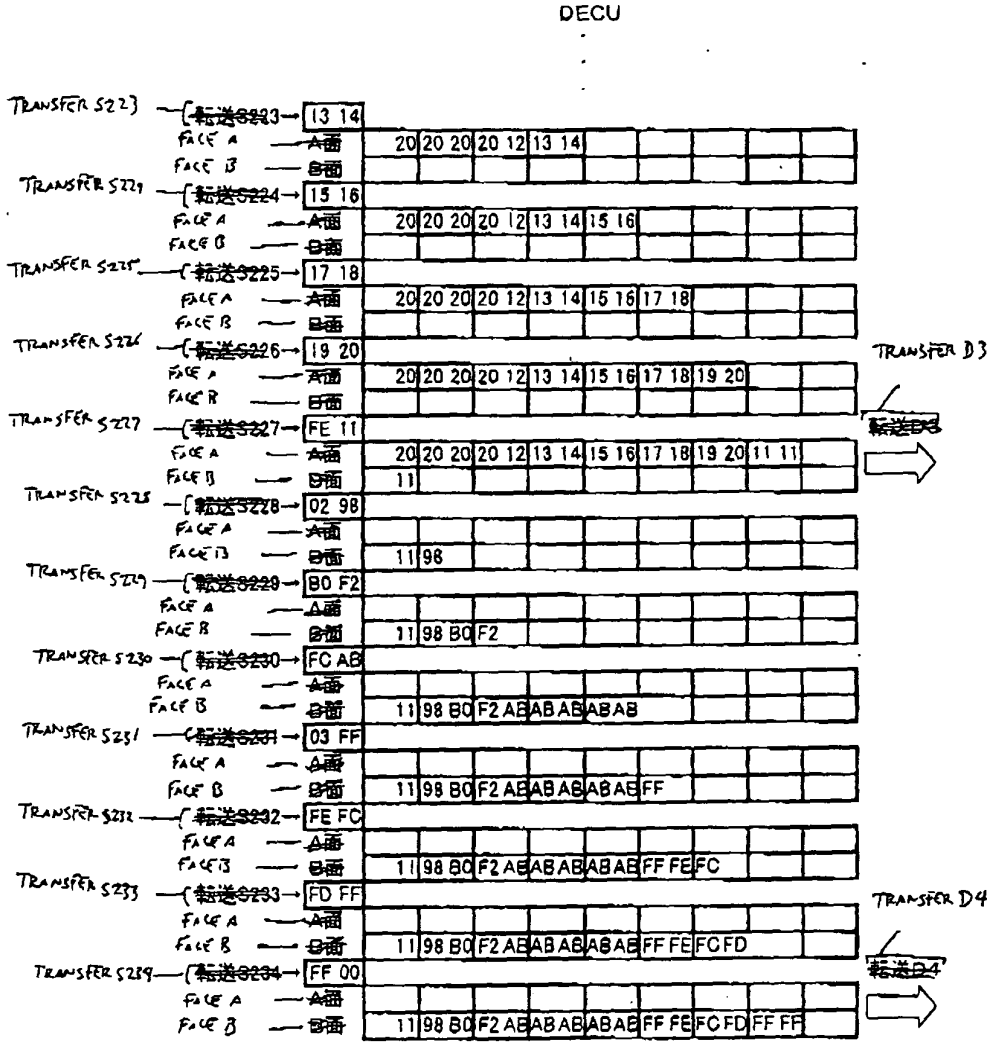


FIG. 28

図 28

貴社No. F008765
弊所No. EP9003



SETTING CONDITION
VERTICAL LINE REARRANGEMENT PERFORMED
TOTAL NUMBER OF DEVELOPED BYTES : 64 BYTES (16×4)
NUMBER OF BYTES IN 1 LINE : 16 BYTES
NUMBER OF DEVELOPED LINES : 4 LINES

資社No. F008765
務所No. EP9003

設定条件
ライン縦並び変換あり
総展開バイト数:64バイト(16×4)
1ラインバイト数:16バイト
展開ライン数:4ライン

LOCAL MEMORY

IMAGE 1

D1 |

01 01	00 00	00 00	00 00	...	00 00
01 02	00 00	00 00	00 00	...	00 00
78 55	00 00	00 00	00 00	...	00 00
44 FF	00 00	00 00	00 00	...	00 00
FF FF	00 00	00 00	00 00	...	00 00
FF FF	00 00	00 00	00 00	...	00 00
FF 11	00 00	00 00	00 00	...	00 00
11 11	00 00	00 00	00 00	...	00 00

FIG. 29A

IMAGE 2

D2 |

66 12	00 00	00 00	00 00	...	00 00
77 45	00 00	00 00	00 00	...	00 00
89 10	00 00	00 00	00 00	...	00 00
55 10	00 00	00 00	00 00	...	00 00
10 10	00 00	00 00	00 00	...	00 00
10 10	00 00	00 00	00 00	...	00 00
10 20	00 00	00 00	00 00	...	00 00
20 20	00 00	00 00	00 00	...	00 00

FIG. 29B

IMAGE 1

D3 |

01 01	20 20	00 00	00 00	...	00 00
01 02	20 20	00 00	00 00	...	00 00
78 55	12 13	00 00	00 00	...	00 00
44 FF	14 15	00 00	00 00	...	00 00
FF FF	16 17	00 00	00 00	...	00 00
FF FF	18 19	00 00	00 00	...	00 00
FF 11	20 11	00 00	00 00	...	00 00
11 11	11 11	00 00	00 00	...	00 00

FIG. 29C

IMAGE 2

D4 |

66 12	11 98	00 00	00 00	...	00 00
77 45	B0 F2	00 00	00 00	...	00 00
89 10	AB AB	00 00	00 00	...	00 00
55 10	AB AB	00 00	00 00	...	00 00
10 10	AB FF	00 00	00 00	...	00 00
10 10	FE FC	00 00	00 00	...	00 00
10 20	FD FF	00 00	00 00	...	00 00
20 20	FF FF	00 00	00 00	...	00 00

FIG. 29D

SETTING CONDITION
NO VERTICAL LINE REARRANGEMENT
TOTAL NUMBER OF DEVELOPED BYTES: 64 BYTES (16×4)
NUMBER OF BYTES IN 1 LINE: 16 BYTES
NUMBER OF DEVELOPED LINES: 4 LINES

設定条件
ライン並び変換なし
展開バイト数:64バイト(16×4)
1ラインバイト数:16バイト
展開ライン数:4ライン

LOCAL MEMORY

IMAGE 1

(a) D1→

01	01	01	02	78	55	44	FF
FF	FF	FF	FF	FF	11	11	11
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

FIG. 30A

IMAGE 2

(b) D2→

66	12	77	45	89	10	55	10
10	10	10	10	10	20	20	20
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

FIG. 30B

IMAGE 1

(c)

01	01	01	02	78	55	44	FF
FF	FF	FF	FF	FF	11	11	11
20	20	20	20	12	13	14	15
16	17	18	19	20	11	11	11
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

D3

FIG. 30C

IMAGE 2

(d) D4→

66	12	77	45	89	10	55	10
10	10	10	10	10	20	20	20
11	98	80	F2	AB	AB	AB	AB
AB	FF	FE	FC	FD	FF	FF	FF
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00
00	00	00	00	00	00	00	00

FIG. 30D

SETTING CONDITION
VERTICAL LINE REARRANGEMENT PERFORMED
TOTAL NUMBER OF DEVELOPED BYTES: 60 BYTES (15X4)
NUMBER OF BYTES IN 1 LINE: 15 BYTES
NUMBER OF DEVELOPED LINES: 4 LINES

~~図31~~

設定条件
ライン並び替え変換あり
総展開バイト数:60バイト(15×4)
1ラインバイト数:15バイト
展開ライン数:4ライン

~~ローカルメモリ~~ LOCAL MEMORY

~~イメージ1~~ IMAGE 1

D1↓

01 01	00 00	00 00	00 00	…	00 00
01 02	00 00	00 00	00 00	…	00 00
78 55	00 00	00 00	00 00	…	00 00
44 FF	00 00	00 00	00 00	…	00 00
FF FF	00 00	00 00	00 00	…	00 00
FF FF	00 00	00 00	00 00	…	00 00
FF 11	00 00	00 00	00 00	…	00 00
11 00	00 00	00 00	00 00	…	00 00

~~(a)~~

FIG. 31A

~~イメージ2~~ IMAGE 2

D2↓

66 12	00 00	00 00	00 00	…	00 00
77 45	00 00	00 00	00 00	…	00 00
89 10	00 00	00 00	00 00	…	00 00
55 10	00 00	00 00	00 00	…	00 00
10 10	00 00	00 00	00 00	…	00 00
10 10	00 00	00 00	00 00	…	00 00
10 20	00 00	00 00	00 00	…	00 00
20 00	00 00	00 00	00 00	…	00 00

~~(b)~~

FIG. 31B

~~イメージ1~~ IMAGE 1

D3↓

01 01	20 20	00 00	00 00	…	00 00
01 02	20 20	00 00	00 00	…	00 00
78 55	12 13	00 00	00 00	…	00 00
44 FF	14 15	00 00	00 00	…	00 00
FF FF	16 17	00 00	00 00	…	00 00
FF FF	18 19	00 00	00 00	…	00 00
FF 11	20 11	00 00	00 00	…	00 00
11 00	11 00	00 00	00 00	…	00 00

(c)

FIG. 31C

~~イメージ2~~ IMAGE 2

D4↓

66 12	11 98	00 00	00 00	…	00 00
77 45	80 F2	00 00	00 00	…	00 00
89 10	AB AB	00 00	00 00	…	00 00
55 10	AB AB	00 00	00 00	…	00 00
10 10	AB FF	00 00	00 00	…	00 00
10 10	FE FC	00 00	00 00	…	00 00
10 20	FD FF	00 00	00 00	…	00 00
20 00	FF 00	00 00	00 00	…	00 00

~~(d)~~

FIG. 31D

SETTING CONDITION
NO VERTICAL LINE REARRANGEMENT
TOTAL NUMBER OF DEVELOPED BYTES : 60 BYTES (15X4)
NUMBER OF BYTES IN 1 LINE : 15 BYTES
NUMBER OF DEVELOPED LINES: 4 LINES

設定条件
ライン並び変換なし
総展開バイト数:60バイト(15×4)
1ラインバイト数:15バイト
展開ライン数:4ライン

LOCAL MEMORY

IMAGE 1

D1

01 01	01 02	78 55	44 FF			
FF FF	FF FF	FF 11	11	00		
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			

FIG. 32A

IMAGE 2

D2

66 12	77 45	89 10	55 10			
10 10	10 10	10 20	20	00		
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			

FIG. 32B

IMAGE 1

D3

01 01	01 02	78 55	44 FF			
FF FF	FF FF	FF 11	11	00		
20 20	20 20	12 13	14 15			
16 17	18 19	20 11	11	00		
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			

FIG. 32C

IMAGE 2

D4

66 12	77 45	89 10	55 10			
10 10	10 10	10 20	20	00		
11 98	B0 F2	AB AB	AB AB			
AB FF	FE FC	FD FF	FF	00		
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			
00 00	00 00	00 00	00 00			

FIG. 32D

SETTING CONDITION
VERTICAL LINE REARRANGEMENT PERFORMED
TOTAL NUMBER OF DEVELOPED BYTES: 64 BYTES (16×4)
NUMBER OF BYTES IN 1 LINE: 16 BYTES
NUMBER OF DEVELOPED LINES: 4 LINES

~~図 33~~

貴社No. ~~F008765~~
弊所No. EP9003

設定条件
ライン並び変換あり
総展開バイト数:64バイト(16×4)
1ラインバイト数:16バイト
展開ライン数:4ライン

~~ローカルメモリ~~ LOCAL MEMORY

~~イメージ1~~ IMAGE 1

D1↓

00	01	00	00	00	00	00	00	…	00	00						
01	01	00	00	00	00	00	00	…	00	00						
02	78	00	00	00	00	00	00	…	00	00						
55	44	00	00	00	00	00	00	…	00	00						
FF	FF	00	00	00	00	00	00	…	00	00						
FF	FF	00	00	00	00	00	00	…	00	00						
FF	FF	00	00	00	00	00	00	…	00	00						
11	11	00	00	00	00	00	00	…	00	00						
11	00	00	00	00	00	00	00	…	00	00						

~~(a)~~

FIG. 33A

~~イメージ2~~ IMAGE 2

D2↓

00	66	00	00	00	00	00	00	…	00	00						
12	77	00	00	00	00	00	00	…	00	00						
45	89	00	00	00	00	00	00	…	00	00						
10	55	00	00	00	00	00	00	…	00	00						
10	10	00	00	00	00	00	00	…	00	00						
10	10	00	00	00	00	00	00	…	00	00						
10	10	00	00	00	00	00	00	…	00	00						
20	20	00	00	00	00	00	00	…	00	00						
20	00	00	00	00	00	00	00	…	00	00						

~~(b)~~

FIG. 33B

~~イメージ1~~ IMAGE 1

D3↓

00	01	00	20	00	00	00	00	…	00	00						
01	01	20	20	00	00	00	00	…	00	00						
02	78	20	12	00	00	00	00	…	00	00						
55	44	13	14	00	00	00	00	…	00	00						
FF	FF	15	16	00	00	00	00	…	00	00						
FF	FF	17	18	00	00	00	00	…	00	00						
FF	FF	19	20	00	00	00	00	…	00	00						
11	11	11	11	00	00	00	00	…	00	00						
11	00	11	00	00	00	00	00	…	00	00						

~~(c)~~

FIG. 33C

~~イメージ2~~ IMAGE 2

D4↓

00	66	00	11	00	00	00	00	…	00	00						
12	77	98	BC	00	00	00	00	…	00	00						
45	89	F2	AE	00	00	00	00	…	00	00						
10	55	AB	AE	00	00	00	00	…	00	00						
10	10	AB	AE	00	00	00	00	…	00	00						
10	10	FF	FE	00	00	00	00	…	00	00						
10	10	FC	FD	00	00	00	00	…	00	00						
20	20	FF	FF	00	00	00	00	…	00	00						
20	00	FF	00	00	00	00	00	…	00	00						

~~(d)~~

FIG. 33D

SETTING CONDITION
VERTICAL LINE REARRANGEMENT PERFORMED
TOTAL NUMBER OF DEVELOPED BYTES: 60 BYTES (15X4)
NUMBER OF BYTES IN 1 LINE: 15 BYTES
NUMBER OF DEVELOPED LINES: 4 LINES

~~図一34~~

~~設定条件~~
~~ライン並び変換あり~~
~~総展開バイト数~~:60バイト(15×4)
~~1ラインバイト数~~:15バイト
~~展開ライン数~~:4ライン

~~自社No.~~ F008765
~~弊所No.~~ EP9003

~~(a)~~

FIG. 34A

~~ローカルメモリ~~ LOCAL MEMORY

D1 ~~イメージ1~~ IMAGE 1

00	01	00	00	00	00	00	00	… 00 00
01	01	00	00	00	00	00	00	… 00 00
02	78	00	00	00	00	00	00	… 00 00
55	44	00	00	00	00	00	00	… 00 00
FF	FF	00	00	00	00	00	00	… 00 00
FF	FF	00	00	00	00	00	00	… 00 00
FF	FF	00	00	00	00	00	00	… 00 00
11	11	00	00	00	00	00	00	… 00 00

~~(b)~~

FIG. 34B

D2 ~~イメージ2~~ IMAGE 2

00	66	00	00	00	00	00	00	… 00 00
12	77	00	00	00	00	00	00	… 00 00
45	89	00	00	00	00	00	00	… 00 00
10	55	00	00	00	00	00	00	… 00 00
10	10	00	00	00	00	00	00	… 00 00
10	10	00	00	00	00	00	00	… 00 00
10	10	00	00	00	00	00	00	… 00 00
20	20	00	00	00	00	00	00	… 00 00

~~(c)~~

FIG. 34C

D3 ~~イメージ1~~ IMAGE 1

00	01	00	20	00	00	00	00	… 00 00
01	01	20	20	00	00	00	00	… 00 00
02	78	20	12	00	00	00	00	… 00 00
55	44	13	14	00	00	00	00	… 00 00
FF	FF	15	16	00	00	00	00	… 00 00
FF	FF	17	18	00	00	00	00	… 00 00
FF	FF	19	20	00	00	00	00	… 00 00
11	11	11	11	00	00	00	00	… 00 00

~~(d)~~

FIG. 34D

D4 ~~イメージ2~~ IMAGE 2

00	66	00	11	00	00	00	00	… 00 00
12	77	98	BC	00	00	00	00	… 00 00
45	89	F2	AB	00	00	00	00	… 00 00
10	55	AB	AB	00	00	00	00	… 00 00
10	10	AB	AB	00	00	00	00	… 00 00
10	10	FF	FE	00	00	00	00	… 00 00
10	10	FC	FD	00	00	00	00	… 00 00
20	20	FF	FF	00	00	00	00	… 00 00

FIG. 35

OPERATION CONDITION

MAIN MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN EVEN ADDRESS

LOCAL MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN EVEN ADDRESS

貴社No. F008765
弊所No. EP9003

動作条件
メインメモリ側：イメージデータの開始アドレス 偶数アドレス
ローカルメモリ側：イメージデータの開始アドレス 偶数アドレス
1ラインバイト数：16バイト

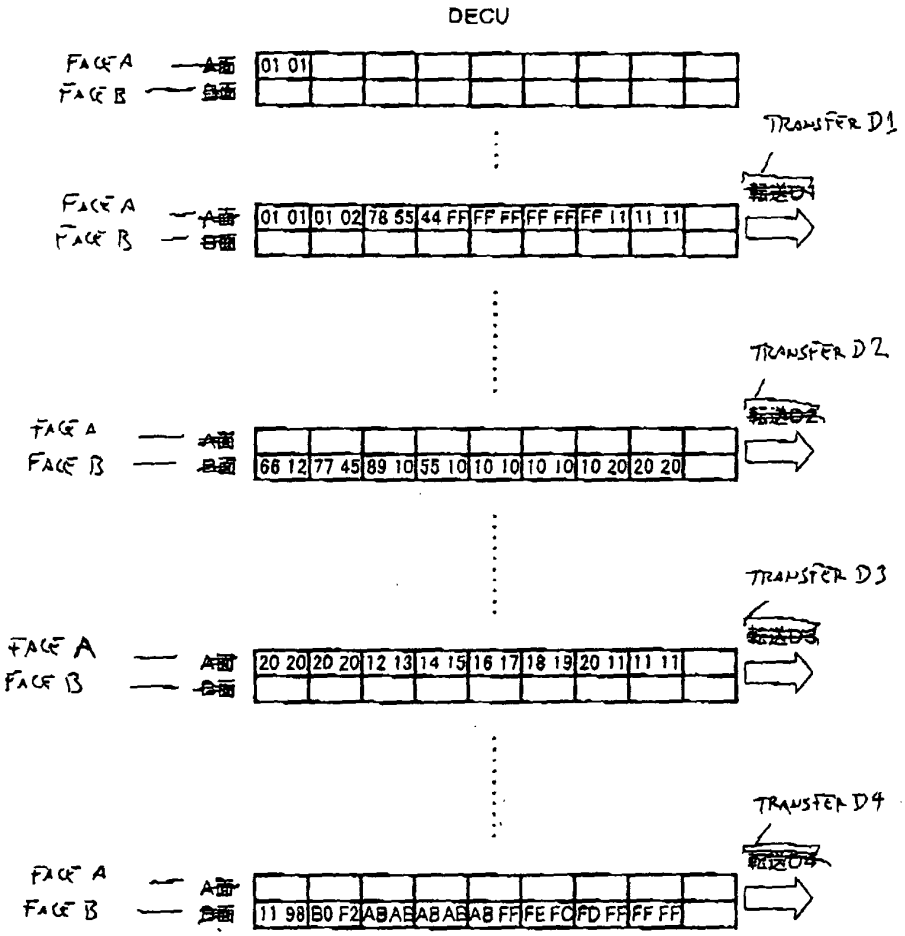
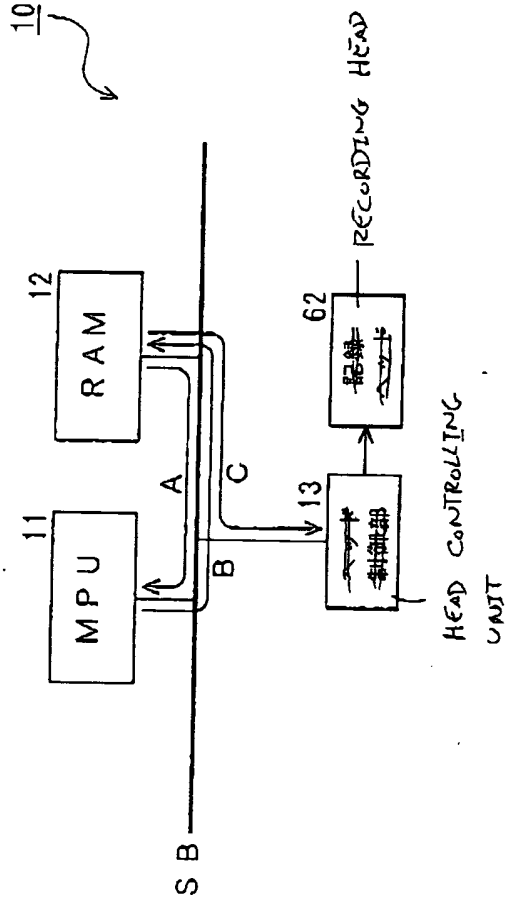


FIG. 36

~~出願No. F008765~~
~~整理No. EP9003~~